


Il sottoscritto Angelo Cotta Ramusino, ai sensi degli art.46 e 47 DPR 445/2000, consapevole delle sanzioni penali previste dall'art.76 del DPR 445/2000 e successive modificazioni ed integrazioni per le ipotesi di falsità in atti e dichiarazioni mendaci, dichiara sotto la propria responsabilità:

INFORMAZIONI PERSONALI



Angelo Cotta Ramusino

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Data di nascita 14/12/19262 | Nazionalità Italiana

CANDIDATO AL RUOLO DI

Componente di commissione esaminatrice per un concorso INFN

POSIZIONE RICOPERTA

Dirigente Tecnologo della Sezione INFN di Ferrara

TITOLO DI STUDIO

Laurea in Ingegneria Elettronica

ESPERIENZA LAVORATIVA

da 2009

Dirigente Tecnologo

ISTITUTO NAZIONALE DI FISICA NUCLEARE - Sezione di Ferrara

Responsabile del Servizio elettronico

Progettazione (hardware e firmware) di sistemi elettronici e circuiti integrati (ASIC) dedicati all'acquisizione dati da rivelatori impiegati in Fisica Nucleare e delle Alte Energie

da 2005 a 2009

Primo Tecnologo

ISTITUTO NAZIONALE DI FISICA NUCLEARE - Sezione di Ferrara

Responsabile del Servizio elettronico

Progettazione (hardware e firmware) di sistemi elettronici dedicati all'acquisizione dati per esperimenti di Fisica Nucleare e delle Alte Energie

da 1998 a tutto 2004

Tecnologo

ISTITUTO NAZIONALE DI FISICA NUCLEARE - Sezione di Ferrara

Responsabile del Servizio elettronico di Sezione (dal 1999)

Progettazione (hardware e firmware) di sistemi elettronici dedicati all'acquisizione dati per esperimenti di Fisica Nucleare e delle Alte Energie

da 1991 a tutto 1997

Tecnologo

ISTITUTO NAZIONALE DI FISICA NUCLEARE - Sezione di Bologna

Progettazione elettronica analogica e digitale presso il Servizio elettronico

da 1988 a 1991

Guest Engineer

Fermi National Accelerator Laboratory – Batavia, IL 60510, USA

Progettazione elettronica analogica e digitale presso il Physics Department

da 1986 a 1988

Summer student / Guest Scientist

Fermi National Accelerator Laboratory – Batavia, IL 60510, USA

Progettazione presso il Physics Department di moduli elettronici analogici per il sistema di trigger sull'energia del calorimetro adronico dell'esperimento E687 (lavoro descritto nella tesi di laurea)

ISTRUZIONE E FORMAZIONE

- da 1981 a 1988 **Laurea in Ingegneria Elettronica**
conseguita con il punteggio di 110 / 110 L presso Università degli Studi di Pavia, Pavia (PV), Italia
- da 1976 a 1981 **Diploma di Perito Industriale ad indirizzo elettronico**
conseguito con il punteggio di 60/60 presso I.T.I.S. " G. Omar ", Novara (NO), Italia

COMPETENZE PERSONALI

Lingua madre Italiana

Altre lingue

	COMPRESIONE		PARLATO		PRODUZIONE SCRITTA
	Ascolto	Lettura	Interazione	Produzione orale	
Inglese	C2	C2	C2	C2	C2
Sostituire con il nome del certificato di lingua acquisito. Inserire il livello, se conosciuto					

Livelli: A1/A2: Utente base - B1/B2: Utente intermedio - C1/C2: Utente avanzato
[Quadro Comune Europeo di Riferimento delle Lingue](#)

Competenze comunicative Elaborazione e presentazione di programmi, proposte e stati di avanzamento anche nell'ambito di collaborazioni internazionali. Capacità didattiche maturate nell'insegnamento del corso di Fisica dei Dispositivi Elettronici del CdL in Fisica dell'Università degli Studi di Ferrara e nella supervisione di lavori di tesi e tirocinio.

Competenze organizzative e gestionali Competenze, sviluppate nell'esercizio del ruolo di coordinatore del servizio elettronico di Sezione, nell'analisi dei requisiti e nella sintesi di progetti per la realizzazione di sistemi elettronici in collaborazione con i colleghi dipendenti della Sezione, associati Universitari, collaboratori a tempo determinato, tesisti e tirocinanti.

Competenze professionali Competenze professionali acquisite nello svolgimento di attività progettuali e nella copertura di ruoli di coordinamento quali:

- (2015 ad oggi) progettista dei moduli GEM-ROC e responsabile dell'elettronica "off-detector" per l'acquisizione dati dal rivelatore CGEM-IT dell'esperimento BES-III presso il Beijing Electron-Positron Collider II (BEPC II) Pechino, PRC.
- (2012 ad oggi) collaboratore alla progettazione, fabbricazione, controllo qualità e collaudo dell'elettronica di front end che prevede l'impiego di circa 33.000 circuiti integrati CLARO8 specificamente sviluppati per l'upgrade del rivelatore RICH dell'esperimento LHCb presso il CERN.
- (2010 ad oggi) collaboratore e coordinatore della progettazione e realizzazione del sistema di acquisizione dati "off-detector" per il rivelatore "GigaTracker" dell'esperimento NA62 presso il CERN.
- (2008-2012) collaboratore alla progettazione del rivelatore e del sistema di lettura di un prototipo di rivelatore di muoni basato su scintillatore plastico, fibre scintillanti e SiPM per l'esperimento "SuperB".
- (2003-2006) collaboratore e coordinatore della progettazione, realizzazione, installazione del sistema di lettura per il rivelatore di muoni a Limited Streamer Tubes dell'esperimento BaBar, per un totale di circa 12000 canali.
- (2000-2004) collaboratore e coordinatore della progettazione, realizzazione e installazione del sistema di TDC in formato 9U VME per la lettura delle camere a deriva di NA48, per un totale di circa 8000 canali.
- (1997) collaboratore alla progettazione, prototipazione e controllo qualità sulla produzione di alcuni componenti del rivelatore "Time Of Flight" per l'esperimento AMS-1.
- (1996) progettista di una scheda prototipo (su cui e' stata basata la produzione, a cura di C.A.E.N. SpA, di un sistema da 6.000 canali) per 4 canali con funzione di dual gain, 10bit ADC in formato 6U VME per il calorimetro elettromagnetico dell'esperimento HERA-B.
- (1993) Progettista principale di una scheda in formato VME 9U basata sul chip neurale ETANN di Intel, collaudata per la selezione di eventi con Beauty nei dati online dell'esperimento WA92 presso il CERN.

- (1992-1993) collaboratore e coordinatore della progettazione, realizzazione e collaudo di un sistema di alimentazione programmabile, basato su moduli regolatori lineari in formato EuroCard 3U, per il calorimetro al silicio dell'esperimento OPAL al CERN.

Competenza digitale

AUTOVALUTAZIONE				
Elaborazione delle informazioni	Comunicazione	Creazione di Contenuti	Sicurezza	Risoluzione di problemi
Utente avanzato	Utente avanzato	Utente avanzato	Utente autonomo	Utente autonomo

Livelli: Utente base - Utente intermedio - Utente avanzato

Patente di guida Categoria B

ULTERIORI INFORMAZIONI

Pubblicazioni Co-autore di piu' di 40 lavori (contando solo quelli relativi ad elettronica per esperimenti) pubblicati su riviste internazionali e proceedings di conferenze internazionali a cui si aggiungono le presentazioni agli incontri di collaborazione, le note tecniche e gli elaborati tecnici firmati.

Corsi Docente (dall' AA 1999/2000) del corso di Fisica dei dispositivi elettronici, opzionale per il corso di Laurea Magistrale in Fisica dell'Università di Ferrara. Relatore o correlatore per 10 tesi di Laurea triennale in Fisica, in Tecnologie Fisiche Innovative e in Informatica (Uni-Fe). Correlatore di 1 tesi di Laurea in Ingegneria Elettronica (Uni-Fe).

ALLEGATI

Nessun allegato

Dati personali Il sottoscritto dichiara di essere informato, ai sensi del d.lgs. n.196/2003, che i dati personali raccolti saranno trattati anche con strumenti informatici esclusivamente nell'ambito del procedimento per il quale la presente dichiarazione viene resa e per tutti gli adempimenti connessi.

Il sottoscritto acconsente, ai sensi del Regolamento UE 2016/679 e del D.lgs. 196 del 30 giugno 2003, al trattamento dei propri dati personali.

Il sottoscritto acconsente alla pubblicazione del presente curriculum vitae sul sito dell' Ente richiedente.

Ferrara, li 18/06/2019

Il dichiarante,

Angelo Cotta Ramusino

Curriculum Vitae

MICHELA GRECO

Associate Professor, Experimental Physics, University of Turin, Italy (November 2014-present)

Education

2000 PhD in Physics | University of Turin, Italy
1996 Degree in Physics (first class Honours and Honourable Mention) | University of Turin, Italy

Awards

2010 Award for the 2nd Best Communication, Italian Society of Physics (SIF)
1996 Award "Turinetti di Priero Simonis" for the best Physics Thesis, University of Turin
Award "Optime" for the best Physics Thesis, Industrial Union of Turin

Employment History

10/2006-10/2014 Researcher, Experimental Physics | University of Turin, Italy
10/2000-9/2006 INFN researcher with fixed-term contracts | INFN- Genoa, Italy

Academic Activities:

2007-present Member of the Academic Board, Torino Graduate School, PhD Program in Physics

Teaching

2014-present **Laboratory of Advanced Electronics**, MSc Degree in Physics
2012-present **Digital Electronics**, MSc Degree in Physics
2008-present **Materials for Optics**, BA Degree in Optics and Optometry
2013-2018 **Electronics**, MSc Degree in Strategic Science
2008-2012 **Laboratory of Condensed Matter Physics**, BA Degree in Physics
2009-2010 **Physics for cultural heritage**, BA Degree in Science and Technology of Cultural Heritage
2006-2009 **Laboratory of General Physics I**, BA Degree in Optics and Optometry
Solid State Physics, BA Degree in Science and Technology of Cultural Heritage

Supervisor:

2 PhD theses, 13 MSc theses in Physics, 90 BSc theses (15 in Physics, 75 in Optics and Optometry)

Referee:

3 PhD theses in Electronics Engineering and 13 MSc theses in Physics.

Tutoring:

more than 100 curricular internships and 15 extra-curricular stages for the Degree in Optics and Optometry.

Academic Service:

-Member of Physics Dept. Commissions: Research, Didactics, Laboratories, Monitoring and Review
-Responsible for the self-assessment and accreditation (AVA) system for the Degree in Optics and Optometry.
-Website manager for the PhD Program in Physics (<http://dottorato.ph.unito.it>).

Research activity

Michela Greco is author of more than 200 publications in peer-reviewed international journals ([Iris AperTO](#), [Scopus](#)) and takes regularly part to the experiments' collaboration meetings. She has presented the results at international (> 20) and national (> 30) conferences with invited talks, oral and poster contributions.

2015-present member of the BELLE2 collaboration (KEK, Tsukuba, Japan).

The Turin group is involved in the definition and optimization of the tracking algorithms for the simulation and reconstruction software, in data analysis and in the construction of the TOP (time of propagation) detector for the identification of π and K mesons in the central region.

2008-present member of the BESIII (Beijing Electron Spectrometer) collaboration (IHEP, Beijing, China)

BESIII offers a unique experimental setup to investigate charmonium, charm, light hadron and τ physics. The double-ring electron-positron collider (BEPCII) is designed to operate with luminosity $L = 1 \times 10^{33} \text{ cm}^{-2}\text{s}^{-1}$ at 3.7 GeV.

The Turin group has been involved in the construction and installation (2012) of a zero-degree photon detector (ZDD), to be used as luminosity monitor and detector of small angle ISR photons. The Turin group manages the cloud infrastructure, has taken part to an Italy-China technological and scientific exchange project and is involved in the European project (RISE-H2020) for the construction of a Cylindrical Gas Electron Multiplier (CGEM) detector that will replace the BESIII MDC inner tracker, subject to aging.

Michela Greco has been the coordinator of the CGEM IT readout electronics since 2016 (on and off detectors, HV and LV systems, slow control and DAQ), as well as actively participating in the development and characterization of the ASIC TIGER (Torino Integrated Electronics for GEM readout) for the front-end electronics. She has been designed as responsible for the Integration during the installation operation in June 2018.

2006-present *Research development in Optometry*

Michela Greco is involved in the application of digital imaging techniques for the identification of biometric parameters of the anterior chamber of the eye; in the study of the properties of ophthalmic and contact lenses, in particular their wettability before and after application; and in the study of UV and blue light transmittance.

2006-2016 *member of the PANDA (antiProton ANnihilation at DArmstadt) collaboration (GSI, Germany)*

The experiment program concerns the study of hadrons and strong interaction using antiprotons of momentum 1.5 - 15 GeV/c on a fixed target. The Turin group has contributed to the muon detector system, has coordinated the PandaRoot offline software and managed a PANDAgrid node. The Turin group has awarded a Strategic Research Grant, Progetti d'Ateneo 2012, The 3-Dimensional Partonic Structure of Protons and Neutrons (3-D nucleon), Michela Greco has mainly contributed to the development of the self-triggering system for high-rate data acquisition using FPGAs.

2000-2007 *member of the CMS (Compact Muon Solenoid) collaboration (CERN, Switzerland)*

Michela Greco has contributed to the fabrication of the CMS superconducting solenoid magnet. She has supported the application of innovative superconducting materials for cables, magnets and ionizing radiation detectors. She has also gained expertise in the deposition of thin films and photolithographic techniques.

Organization, Coordination, Direction of Research Groups:

2016-present	Coordinator of CGEM-IT Electronics
2016	PI of local research project "Digital imaging for ocular biometry"
2011-2015	Local manager of INFN PANDA_MU research group
2011-2016	Member of PANDA Collaboration Board
2009-2015	Member of PANDA Publication Board
2009-2012	Affiliation to National Institute of Metrological Research (INRiM)
2006-2011	Member of Physics Department Executive Board
2009	Spokesperson of ADAPTIVE experiment (INFN-LNS)
2006	Responsible of the assembly of the sensors on the cold mass of the commissioning of the CMS solenoid at CERN (INFN-Genova)
2000-2006	Member of the Winding Working Group of CMS experiment for the technological transfer (INFN-Genova, Ansaldo Superconduttori SpA)

Scientific Participation in European Projects

- H2020-MSCA-RISE-2014, BESIIICGEM
- EU-FP7 HadronPhysics2, WP3 (FairNet)
- EU-FP7 HadronPhysics3, WP3 (FairNet)
- EU-FP7 HadronPhysics2, WP3 (ENCStudy)
- EU-FP6 Structuring the European Research Area program: NED WGCC

Member of editorial boards

Technical Editor, ASC14, Large Scale, IEEE Trans Applied Superconductivity

Technical Editor, MT23, Large Scale, IEEE Trans Applied Superconductivity

Technical Editor, ASC12, Large Scale, IEEE Trans Applied Superconductivity

Technical Editor, MT22, Large Scale, IEEE Trans Applied Superconductivity

Technical Editor, ASC10, Large Scale, IEEE Trans Applied Superconductivity

Technical Editor, MT20, Large Scale, IEEE Trans Applied Superconductivity

Technical Editor, ASC06, Tests and measurements, IEEE Trans Applied Superconductivity

Referee of Journal of Instrumentation, Nuclear Instruments and Methods in Physics Research (Section A),

Superconductor Science and Technology, IEEE Transactions on Applied Superconductivity

LOC Member

November 2013	International Workshop on Real time, self-triggered front end electronics for multichannel detectors, Torino
April 2013	PANDA FEE/DAQ Workshop, Alba (Cn)
July 2012	PANDA-Computing Workshop, Torino
June 2009	XXIX PANDA Collaboration Meeting, Torino
June 2009	PANDA-DAQT Workshop, Torino
September 2005	MT19, 19th International Conference on Magnet Technology, Genova.

Selection Board Member

INFN Competition 20313/2018

INFN Competition 18585/2017

INFN Competition 18786/2017

INFN Competition 18778/2017

Curriculum of Sebastiano Fabio Schifano

18 giugno 2019

ORCID	0000-0002-0132-9196
RESEARCHER ID	C-3555-2012
SCOPUS AU-ID	12782544700

Education, contracts and Positions

1994		Degree in Computer Science at University of Pisa.
Mar 1995	- Mar 1996	CNR fellowship at Istituto di Elaborazione dell'Informazione (IEI) in Pisa.
Mar 1996	- Mar 1997	Research associate at CNR-IEI institute in Pisa.
Feb 1997	- Feb 1999	Fellowship at Istituto Nazionale di Fisica Nucleare (INFN) in Pisa.
Mar 1999	- Oct 2006	Research associate at INFN (INFN-Pisa first and INFN-Ferrara after).
Nov 2006	- May 2019	Research associate (RU) at University of Ferrara, SSD INF/01.
Jun 2019	- today	Associate Professor (PA) at University of Ferrara, SSD INF/01.

Teaching Activity

I have been teaching the following courses at computer science (Informatica) bachelor (LT) and master (LS) degrees at University of Ferrara since 2002:

2002	- 2010	Computability and complexity (LS).
2002	- 2012	Algorithms and Data Structures (LT).
2003	- 2007	Distributed Systems (LS).
2010	- today	Operating Systems (LT).

Partecipation to National and International Projects

- 01.01.2009 - 31.12.2011: INFN-Ferrara coordinator and task-leader of the EU-FP7 Hadron-Physics2 (HP2).
- 01.01.2010 - 31.12.2014: local coordinator of the JANUS2 project funded by: EU-ERDF/2007-2013, FEDER project UNZA08-4E-020, EU-FP7/2007-2013, ERC grant agreement no.247328, MICINN (Spain), da Junta de Extremadura (contract GR101583).
- 01.01.2012 - 31.12.2014: scientific coordinator of the *Computing on Knights Architectures* (COKA) project funded by INFN.
- 01.01.2012 - 31.12.2014: INFN-Ferrara coordinator and task-leader of the EU-FP7 Hadron-Physics3 (HP3).
- 01.01.2015 - 31.12.2018: INFN-Ferrara coordinator of the *Computing on SoC Architectures* (COSA) funded by INFN.
- 01.09.2017 - 28.02.2021: INFN-Ferrara coordinator of the EU H2020 EuroEXA H2020-EU.1.2.2. - FET Proactive.
- 01.06.2018 - 31.05.2022: member and WP-leader of WP5 *Training* of the EU Joint Doctorate (EJD) STIMULATE project.
- 01.01.2016 - 14.10.2018: member and task coordinator of the EU Erasmus+ Capacity Building *Towards Open Resources Using Services* (TORUS).
- 15.11.2018 - 14.11.2021: member and task coordinator EU Erasmus+ Capacity Building *Master On New Technologies Using Services: BigData/CloudComputing for Environmental Data* (MONTUS).

Presentations at Peer-Reviewed Conference (most recent)

- 10th International Conference on Parallel Processing and Applied Mathematics* (PPAM), September 8-11 2013, Warsaw, Poland. Title: *An optimized Lattice Boltzmann code for BlueGene/Q*.
- 20th International Conference on Computing in High Energy and Nuclear Physics* (CHEP), October 14-18 2013, Amsterdam, The Netherlands. Title: *Computing on knights and kepler architectures*.
- 25th Int. Symp. on Computer Architecture and High Performance Computing* (SBAC-PAD), October 23-26 2013, Porto de Galinhas, Brazil. Title: *Benchmarking GPUs with a Parallel Lattice-Boltzmann Code*.
- International Conference on Computational Science* (ICCS), June 10-12 2014, Cairns, Australia. Title: *A portable OpenCL Lattice Boltzmann code for multi- And many-core processor architectures*.
- International Conference on High Performance Computing and Simulation*, July 20-24 2015, Amsterdam, The Netherlands. Title: *Optimizing Communications in multi-GPU Lattice Boltzmann Simulations*.
- 11th International Conference on Parallel Processing and Applied Mathematics*, September 6-9 2015, Krakow, Poland. Title: *Experience on vectorizing Lattice Boltzmann kernels for multi- and many-core architectures*.

7. *12th International Conference on Parallel Processing and Applied Mathematics*, September 10-13 2017, Lublin, Poland. Title: *Early experience on using Knights Landing processors for Lattice Boltzmann applications.*

Invited talks at conferences and workshops (most recent)

1. *X Seminar on Nuclear, Subnuclear and Applied Physics*, June 2-8, 2013, Alghero, Italy. Title: *Multi- and many-core computing for Physics applications.*
2. *PRACE Summer School Enabling Applications on Intel MIC based Parallel Architectures*, July 8-11, 2013, Casalecchio di Reno, Bologna, Italy. Title: *LBM on multi- and many-core architectures.*
3. *NVIDIA Application Lab Workshop*, July 8-9, 2013, Juelich, Germany. Title: *Benchmarking GPU architectures with Lattice Boltzmann simulations.*
4. *NVIDIA Application Lab Workshop*, June 10-12, 2014, Juelich, Germany. Title: *Portability, Performance and Scalability of LB Codes for Accelerator based Architectures.*
5. *PADC Opening Workshop*, October 12-13, 2015, Juelich, Germany. Title: *Early experience on running GPU-based Lattice Boltzmann simulations on POWER8 systems.*
6. *Introductory School on Parallel Programming and Parallel Architecture for High-Performance Computing*, October 10th, 2016, ICTP Trieste, Italy. Title: *Parallel Approaches To Lattice Boltzmann Methods.*
7. Keynote speaker al workshop *HPCXXL Winter Workshop*, February 8, 2017, CINECA, Bologna (ITALIA). Title: *On usability of HPC systems.*
8. Keynote speaker alla conferenza *The Eight International Conference on Advanced Communications and Computation (INFOCOMP18)* July 22 - 26, 2018, Barcelona, (SPAIN). Titolo del lavoro presentato: *Challenges in Programming Modern Parallel Systems.*

Organizations of conferences and workshops (most recent)

1. Organizer of the workshop *International Workshop on Energy-aware high performance Heterogeneous Architectures and Accelerators (WEHA 2016)*, organized as part of the *The International Conference on High Performance Computing & Simulation (HPCS 2016)*, July 18-22, 2016, Innsbruck, Austria.
2. Member of the program committee of *7th International Workshop in Performance Modeling, Benchmarking and Simulation of High Performance Computer Systems (PMBS16)*, organized as part of the *ACM/IEEE Supercomputing 2016 (SC16)*, Nov. 13, 2016, Salt Lake City, UT (USA).
3. Member of the program committee of the workshop *International Workshop on OpenPOWER for HPC (IWOPH'17)*, part of the *ISC High Performance*, 22 June, 2017 Frankfurt (GERMANY).
4. Member of the program committee of the *The Seventh International Conference on Advanced Communications and Computation (INFOCOMP17)* June 25 - 29, 2017, Venezia (ITALY).
5. Member of the program committee of the *ParCo 2017: Mini-Symposium on Energy Aware Scientific Computing on low power and heterogeneous architectures*, organized as part of the *International Conference on Parallel Computing (ParCo 2017)*, September 12-15, 2017, Bologna, Italia.
6. Member of the program committee of the *8th International Workshop in Performance Modeling, Benchmarking and Simulation of High Performance Computer Systems (PMBS17)*, organizzato come parte della conferenza *ACM/IEEE Supercomputing 2017 (SC17)*, Nov. 13, 2017, Denver, CO (USA).
7. Member of the program committee of the *International Workshop on OpenPOWER for HPC (IWOPH'18)*, organized as part of the *ISC High Performance*, 28 June, 2018 Frankfurt (GERMANY).
8. Member of the program committee of the *The Eight International Conference on Advanced Communications and Computation (INFOCOMP18)* July 22 - 26, 2018, Barcelona, (SPAIN).
9. Member of the program committee and chair of the session *Machine/Deep Learning* of the workshop *9th International Workshop in Performance Modeling, Benchmarking and Simulation of High Performance Computer Systems (PMBS18)*, organized as part of the *ACM/IEEE Supercomputing 2018 (SC18)*, Nov. 12, 2018, Dallas, TX (USA).
10. Organizer and chair of the workshop *Energy-efficient Computing on Parallel Architectures (ECO-PAR18)*, organized as part of the *The Eight International Conference on Advanced Communications and Computation (INFOCOMP18)* July 22 - 26, 2018, Barcelona, (SPAIN).

Participation at European Panel

Italian representative member (2006-2007) of the HPC in Europe Taskforce (HET). The High Performance Computing in Europe Taskforce was formed by representatives of European countries interested in shaping the European High Performance Computing Infrastructure. The initiative was instrumental to the establishment of the PRACE research infrastructure.

Member of editorial board

1. Co-editor, together with S. Bassini, A. Hoisie, D. J. Kerbyson e D. Pleiter, proceedings of the workshop *Future HPC systems: the Challenges of Power-Constrained Performance*, organized as part of the *ACM International Conference on Supercomputing (ICS)* June 25, 2012, San Servolo - Venezia (Italy). ACM, New York, NY, USA, ISBN 978-1-4503-1453-4.
2. Co-editor, together with D. Cesini, of special issue *Energy Aware Scientific Computing on Low Power and Heterogeneous Architectures* on *Journal of Low Power Electronics and Applications* (ISSN 2079-9268).

Awards

1. November 2009 and June 2010 Best GREEN500 HPC system: the HPC system developed within the QPACE project has been awarded as the top entry of the GREEN500 list (www.green500.org).
2. Best Paper Award *On Portability, Performance and Scalability of a MPI OpenCL Lattice Boltzmann Code* presented at *7th Workshop on UnConventional High Performance Computing 2014 (UCHPC 2014)* August 25/26, Porto, Portugal.
3. Best Paper Award *Energy-performance tradeoffs for HPC applications on low power processors* presented at *8th Workshop on UnConventional High Performance Computing 2015 (UCHPC 2015)* August 25, Vienna, Austria.
4. Best Paper Award *Performance Optimization of D3Q19 Lattice Boltzmann Kernels on Intel KNL* presented at *INFO-COMP 2018, The Eighth International Conference on Advanced Communications and Computation* July 22-26, 2018, Barcelona, Spain.

Short Publication List (most relevant)

- [1] C. Bonati, E. Calore, M. D'Elia, M. Mesiti, F. Negro, F. Sanfilippo, S.F. Schifano, G. Silvi e R. Tripiccion. "Portable multi-node LQCD Monte Carlo simulations using OpenACC". In: *International Journal of Modern Physics C* 29.1 (2018). ISSN: 0129-1831. DOI: 10.1142/S0129183118500109. IF(2017)=0.919.
- [2] E. Calore, A. Gabbana, S.F. Schifano e R. Tripiccion. "Design and optimizations of lattice Boltzmann methods for massively parallel GPU-based clusters". In: *Analysis and Applications of Lattice Boltzmann Simulations*. IGI Global, 2018, pp. 54–114. ISBN: 978-152254761-7;1522547606;978-152254760-0. DOI: 10.4018/978-1-5225-4760-0.ch003.
- [3] E. Calore, A. Gabbana, S.F. Schifano e R. Tripiccion. "Early experience on using Knights Landing processors for Lattice Boltzmann applications". In: *Parallel Processing and Applied Mathematics*. Vol. 1077. Lecture Notes in Computer Science. Springer International Publishing, 2018, pp. 1–12. ISBN: 978-3-319-78024-5. DOI: 10.1007/978-3-319-78024-5_45.
- [4] E. Calore, A. Gabbana, S.F. Schifano e R. Tripiccion. "Software and DVFS tuning for performance and energy-efficiency on intel KNL processors". In: *Journal of Low Power Electronics and Applications* 8.2 (2018). ISSN: 20799268. DOI: 10.3390/jlpea8020018. IF(2017)=1.182.
- [5] C. Bonati, S. Coscetti, M. D'Elia, M. Mesiti, F. Negro, E. Calore, S.F. Schifano, G. Silvi e R. Tripiccion. "Design and optimization of a portable LQCD Monte Carlo code using OpenACC". In: *International Journal of Modern Physics C* 28.5 (2017). ISSN: 0129-1831. DOI: 10.1142/S0129183117500632. IF(2017)=0.919.
- [6] E. Calore, A. Gabbana, S.F. Schifano e R. Tripiccion. "Evaluation of DVFS techniques on modern HPC processors and accelerators for energy-aware applications". In: *Concurrency and Computation: Practice and Experience* 29.12 (2017), pp. 1–19. ISSN: 1532-0634. DOI: 10.1002/cpe.4143. IF(2017)=1.114.
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